



Title: GAMING DEVICE HAVING MULTIPLE CHOICE
LARGE AWARD BONUS SCHEME
Inventors: Andrea C. Hughs-Baird et al.
App. No.: 09/686,284
Docket No.: 0112300-143

FIG. 1A

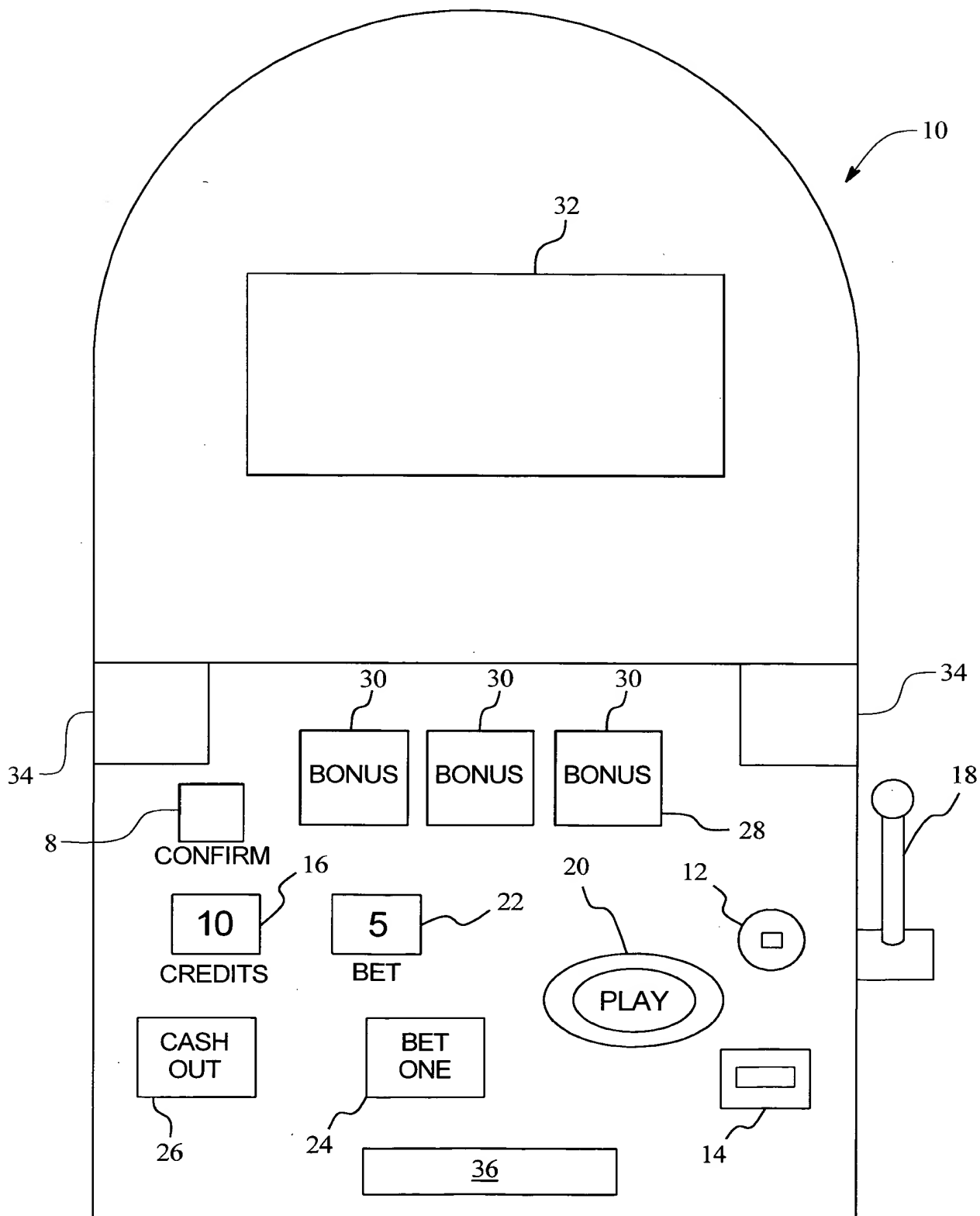
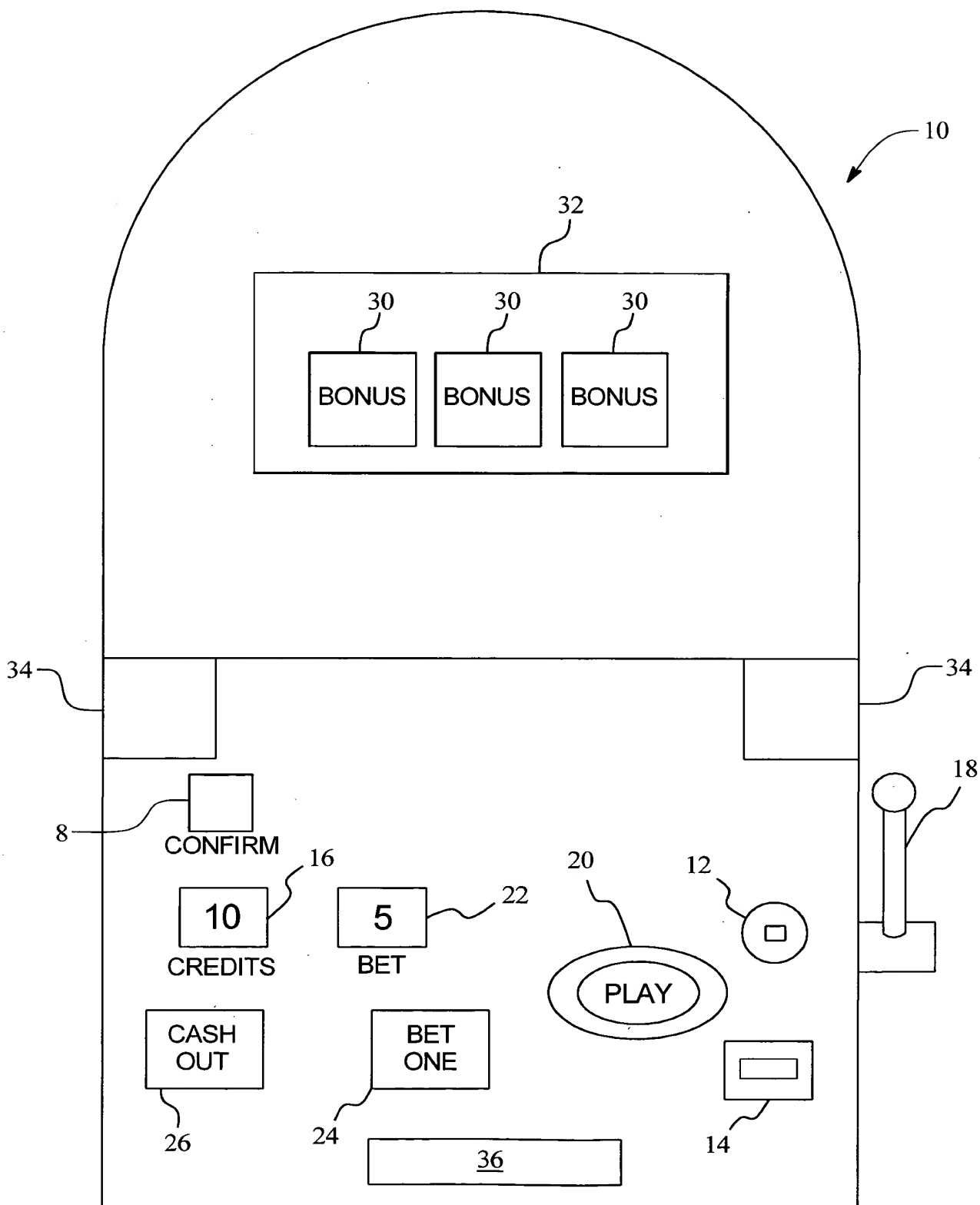
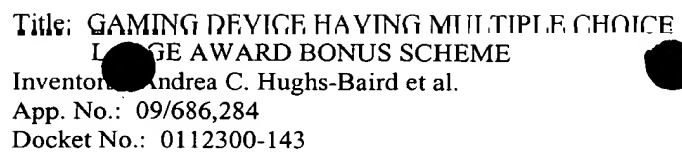




FIG. 1B





```
graph TD
    LAN[LAN, WAN, INTERNET]
    subgraph System
        MP[38: MICRO-PROCESSOR]
        subgraph Memory [40]
            RAM[42: RAM]
            ROM[44: ROM]
        end
        CB[12: COIN / BILL ACCEPTOR]
        ID[33: INPUT DEVICES]
        DD[32: DISPLAY DEVICE]
        S[34: SPEAKERS]
        VC[50: VIDEO CONTROLLER]
        subgraph InputOutput [46, 48]
            TSC[48: TOUCH SCREEN CONTROLLER]
            TS[46: TOUCH SCREEN]
        end
    end
    MP <--> RAM
    MP <--> ROM
    MP <--> CB
    MP <--> ID
    MP --> DD
    MP --> S
    MP <--> VC
    MP <--> TSC
    VC --> TS
```



FIG. 3A

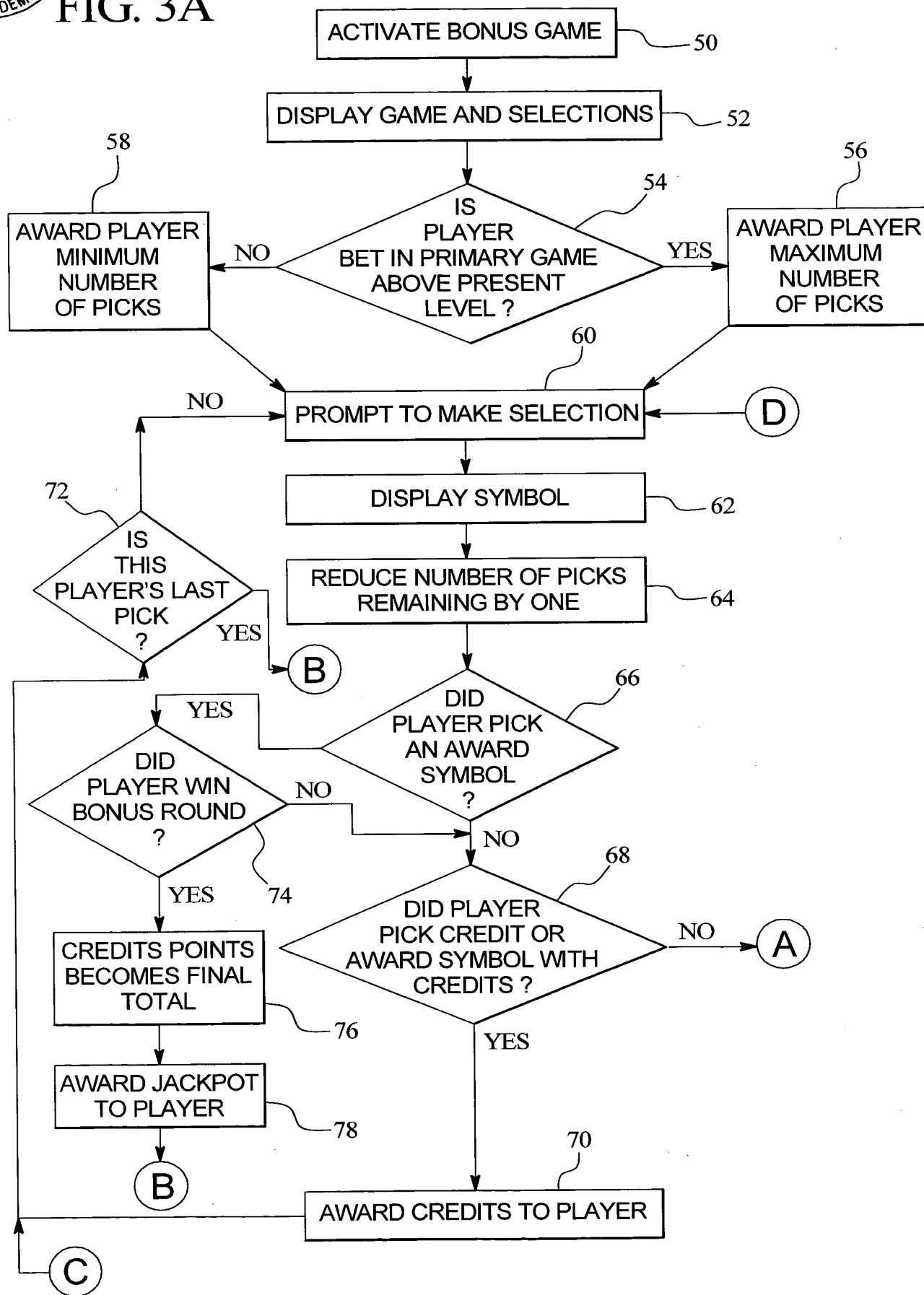




FIG. 3B

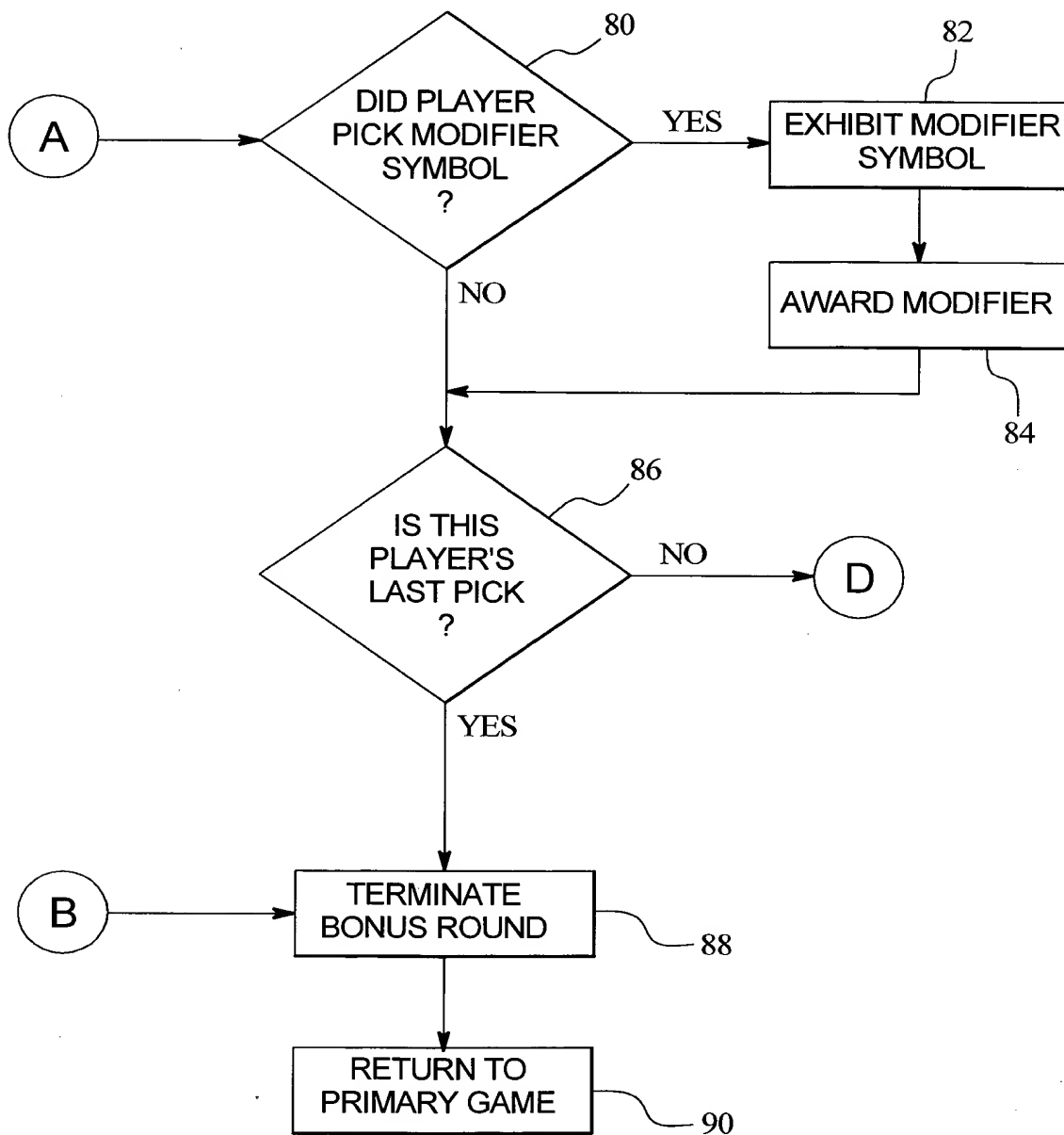




FIG. 4A

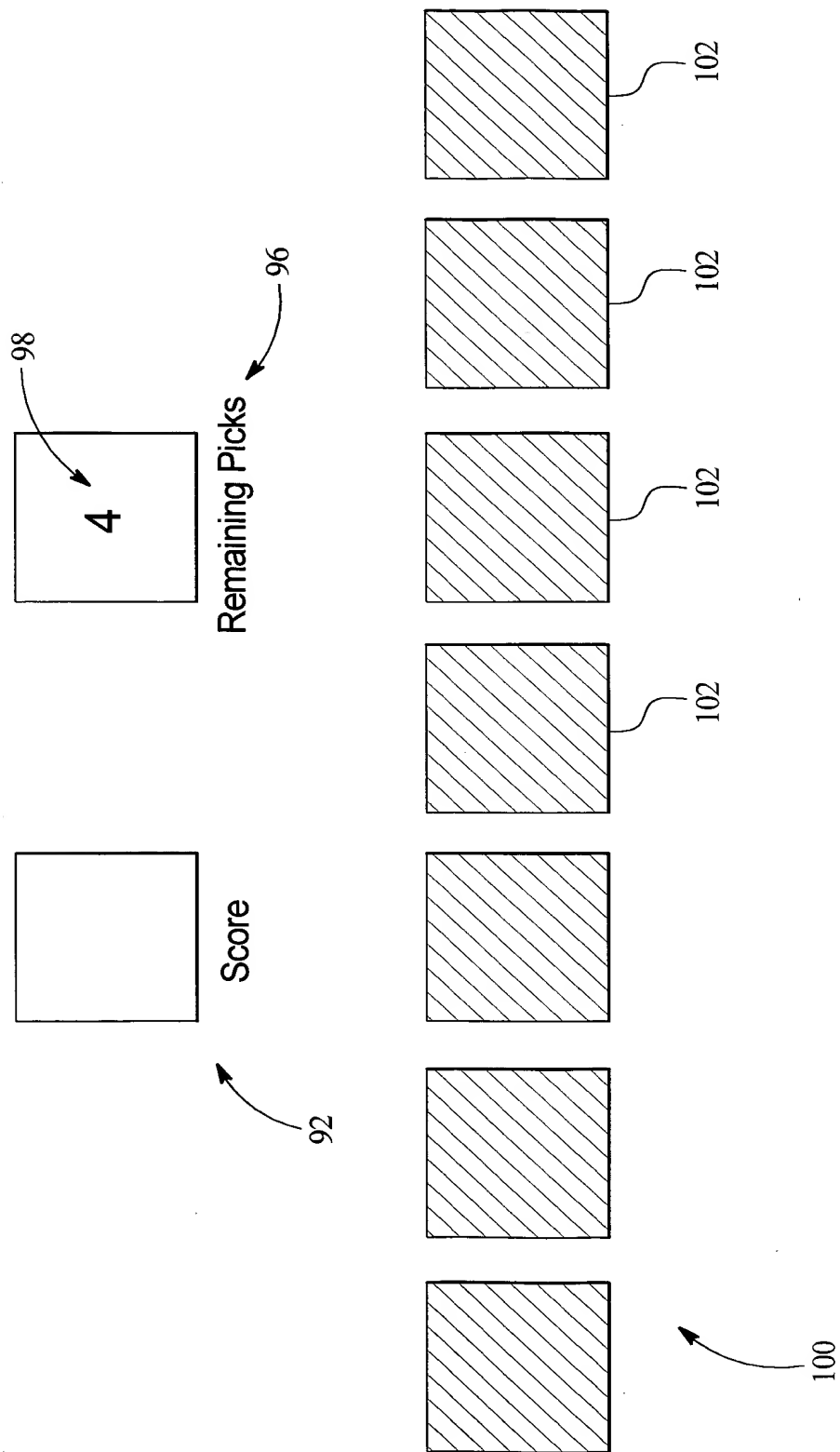




FIG. 4B

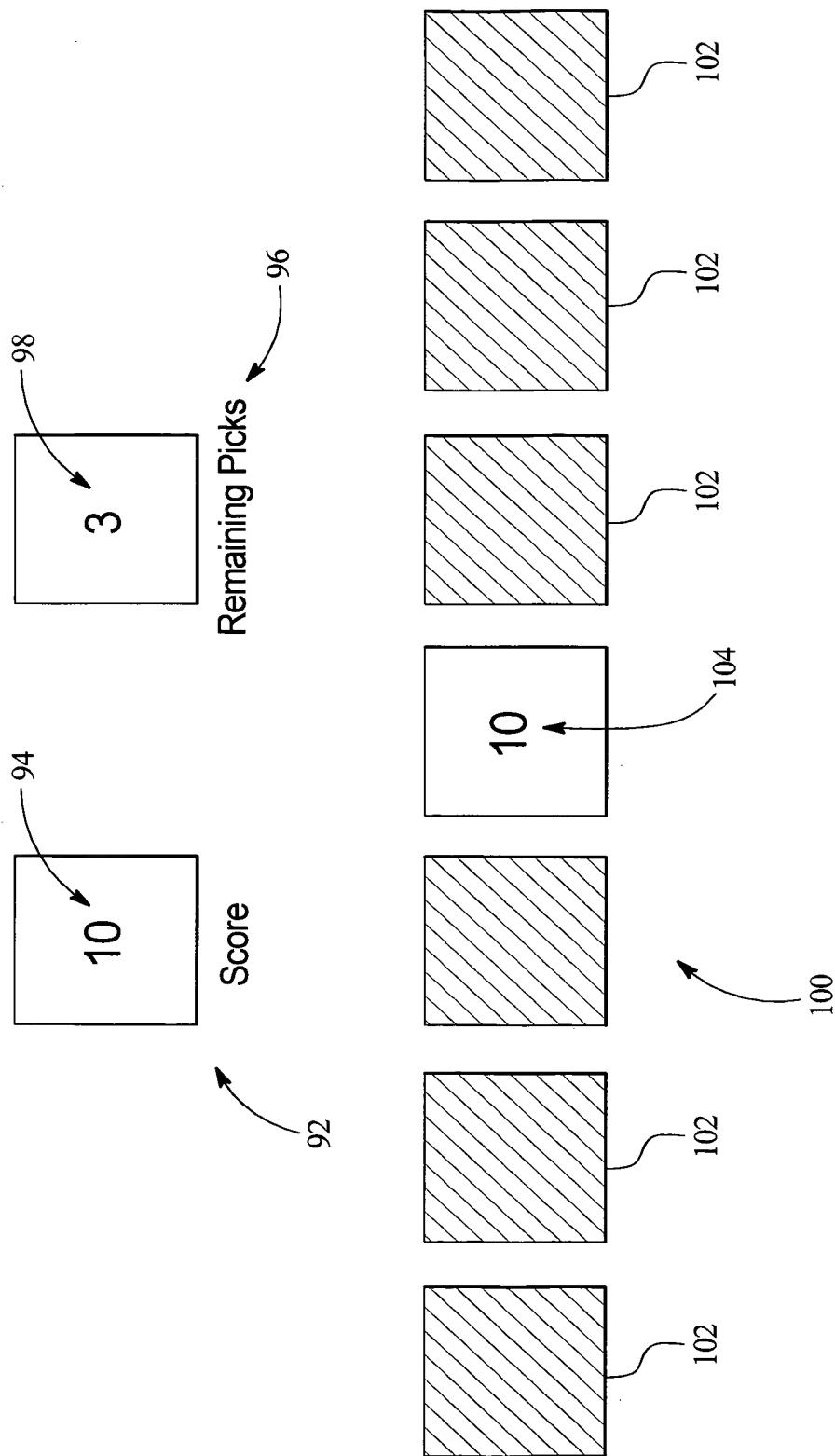




FIG. 4C

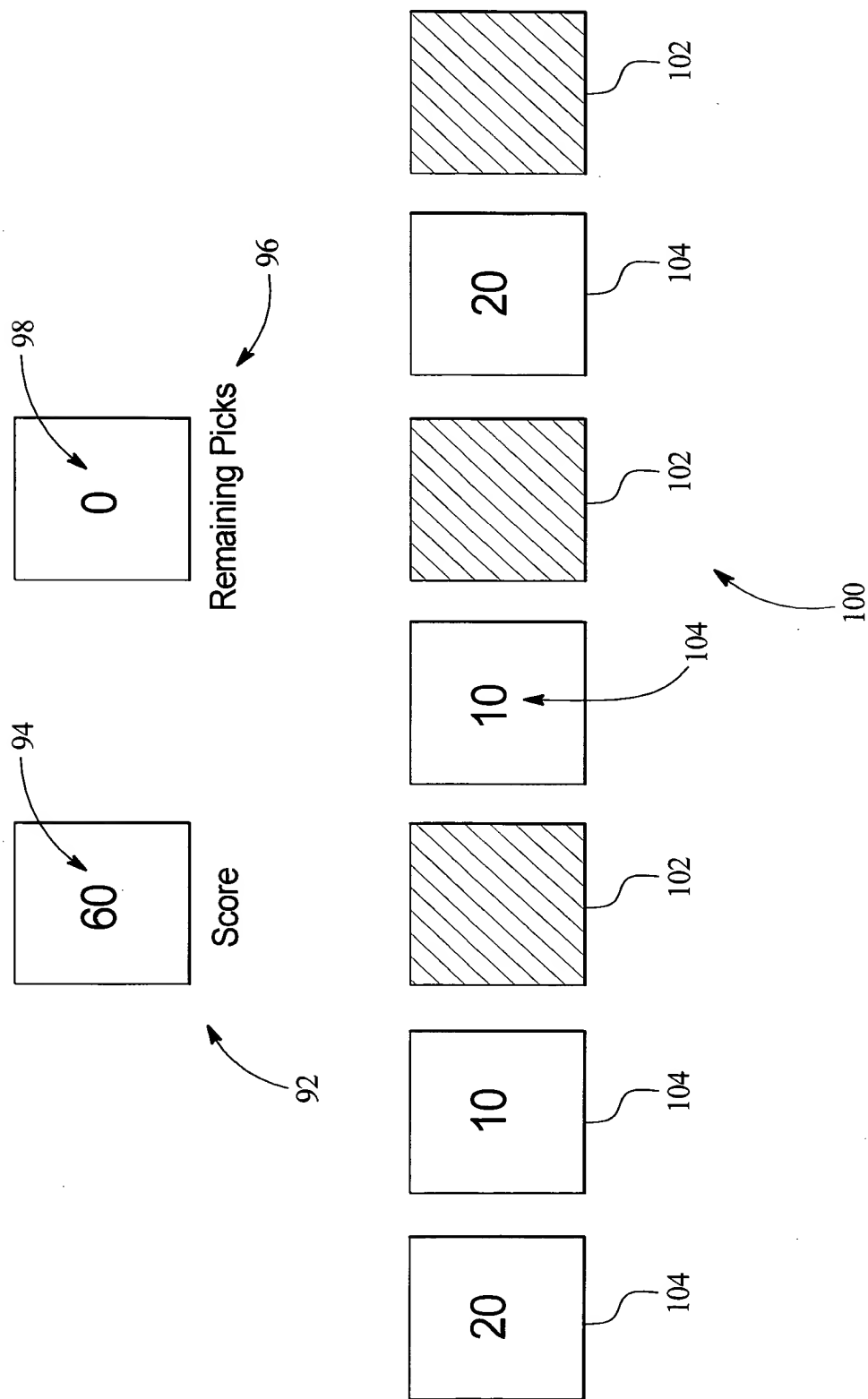




FIG. 5A

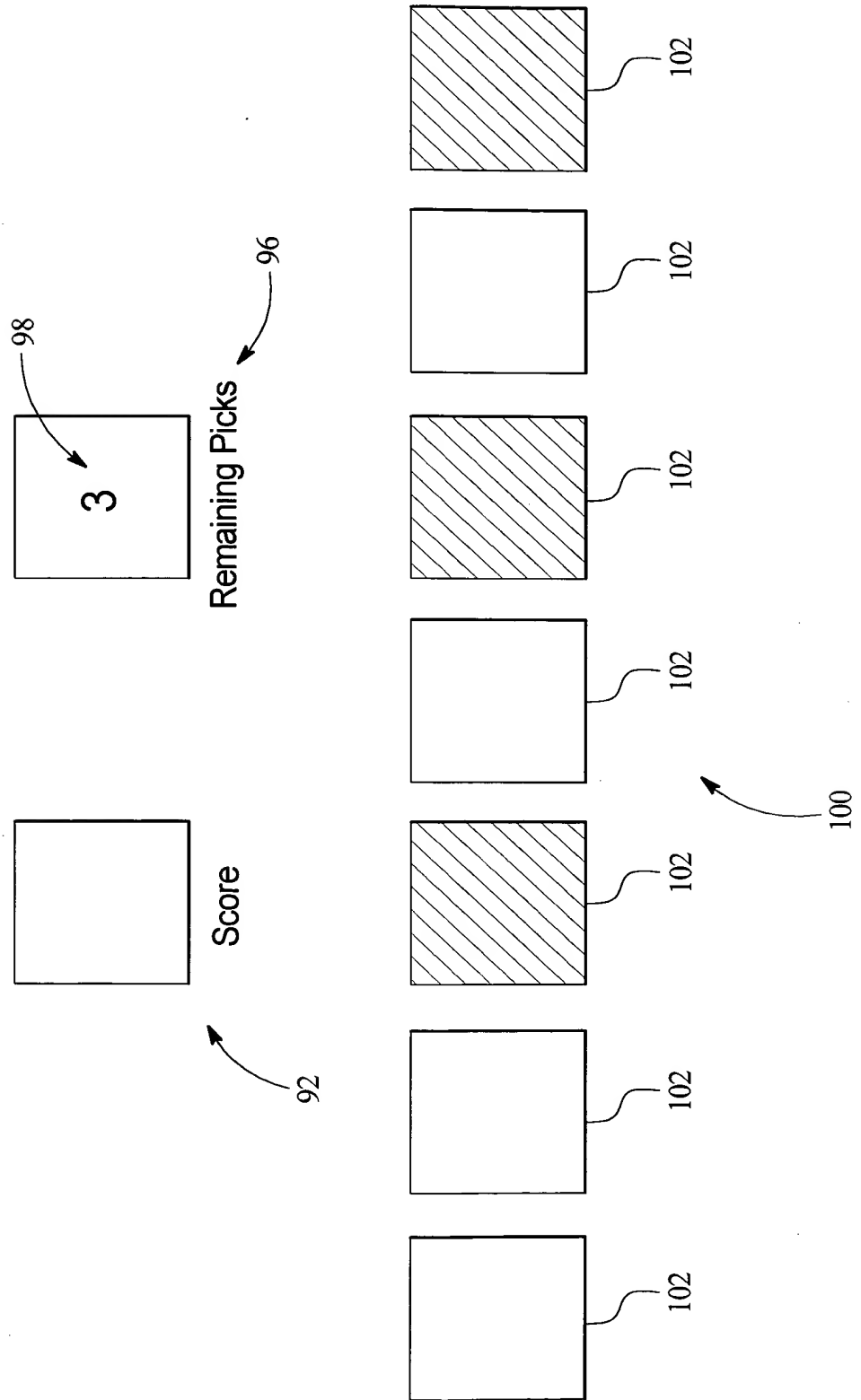
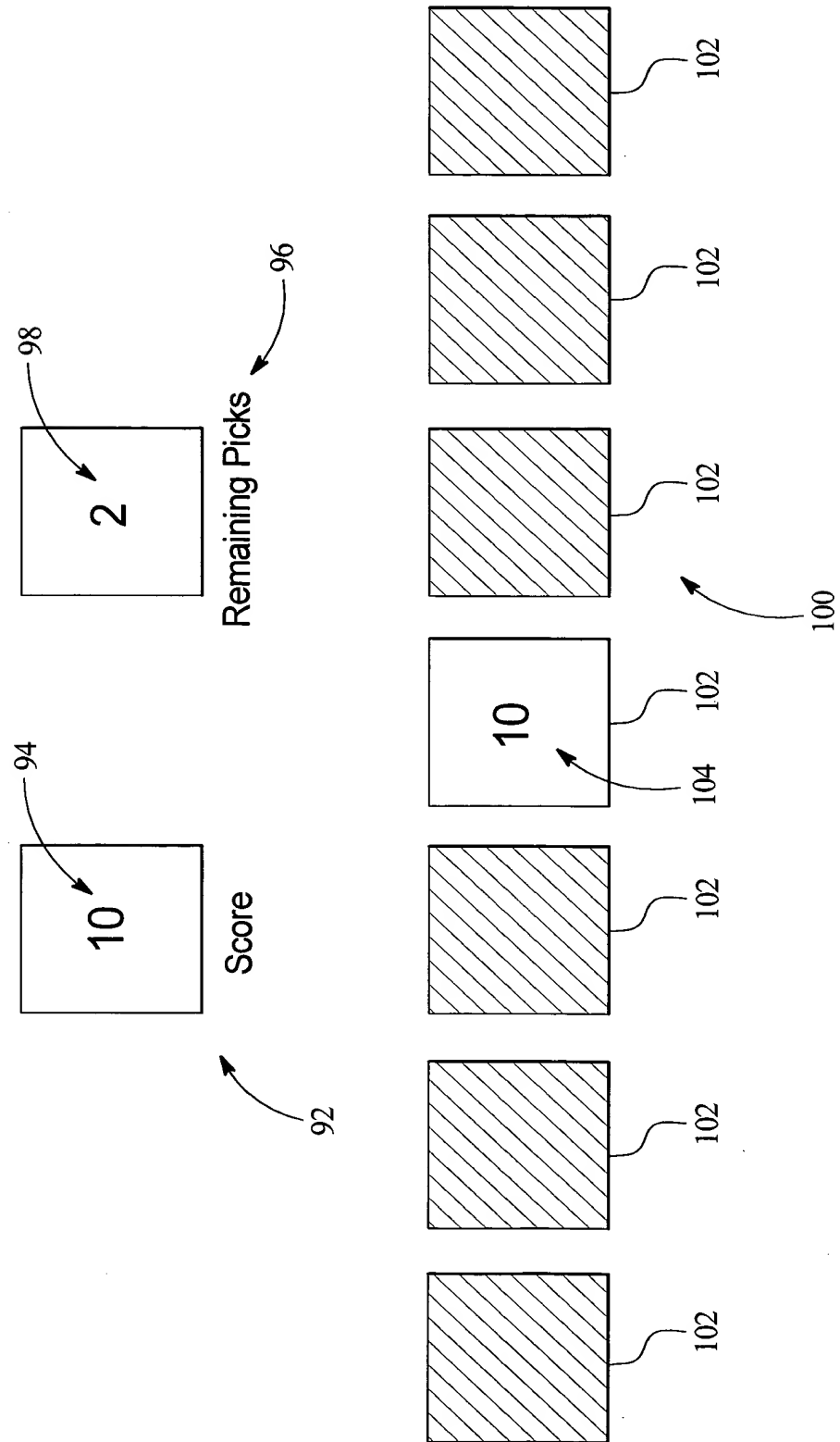




FIG. 5B





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FIG. 5C

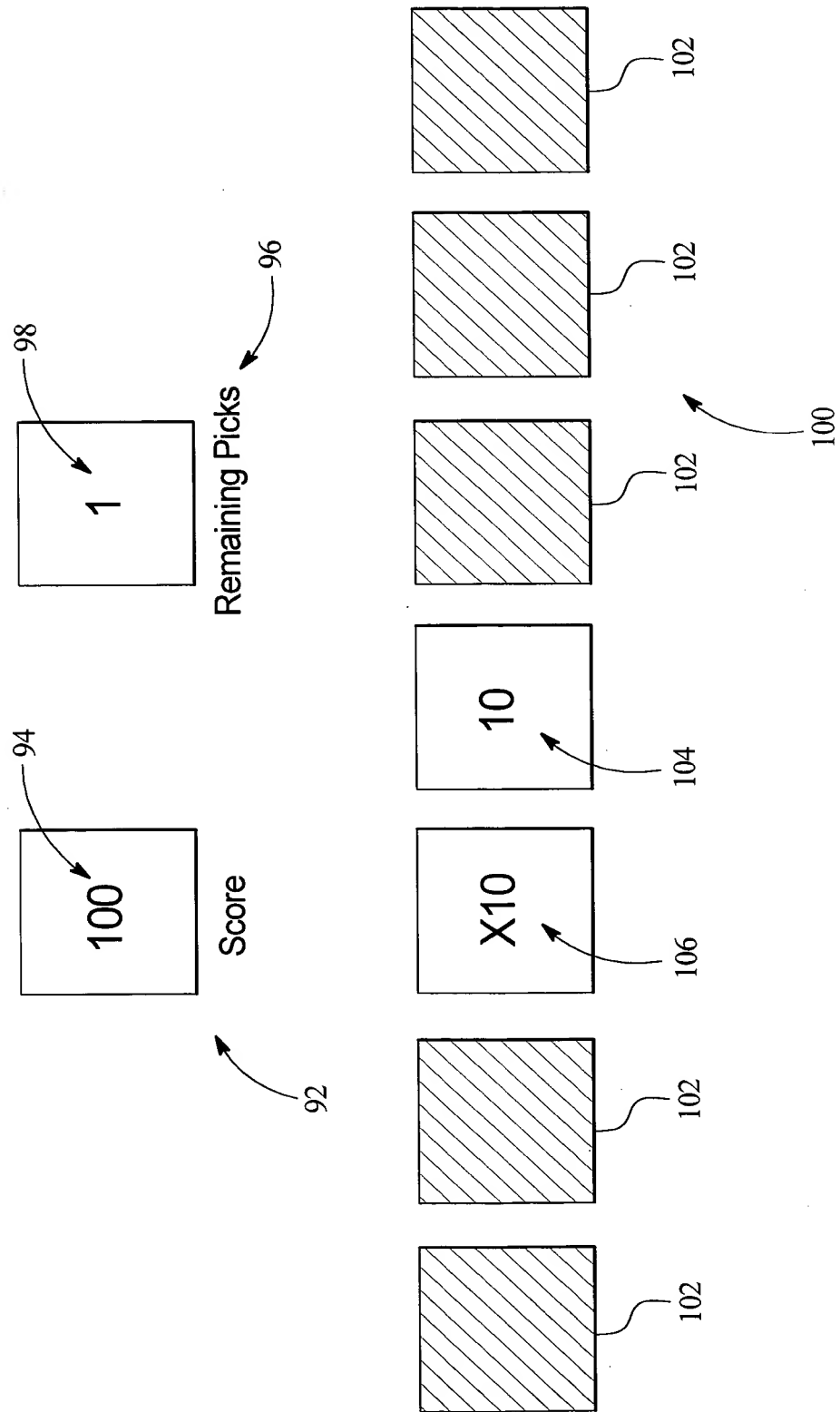




FIG. 5D

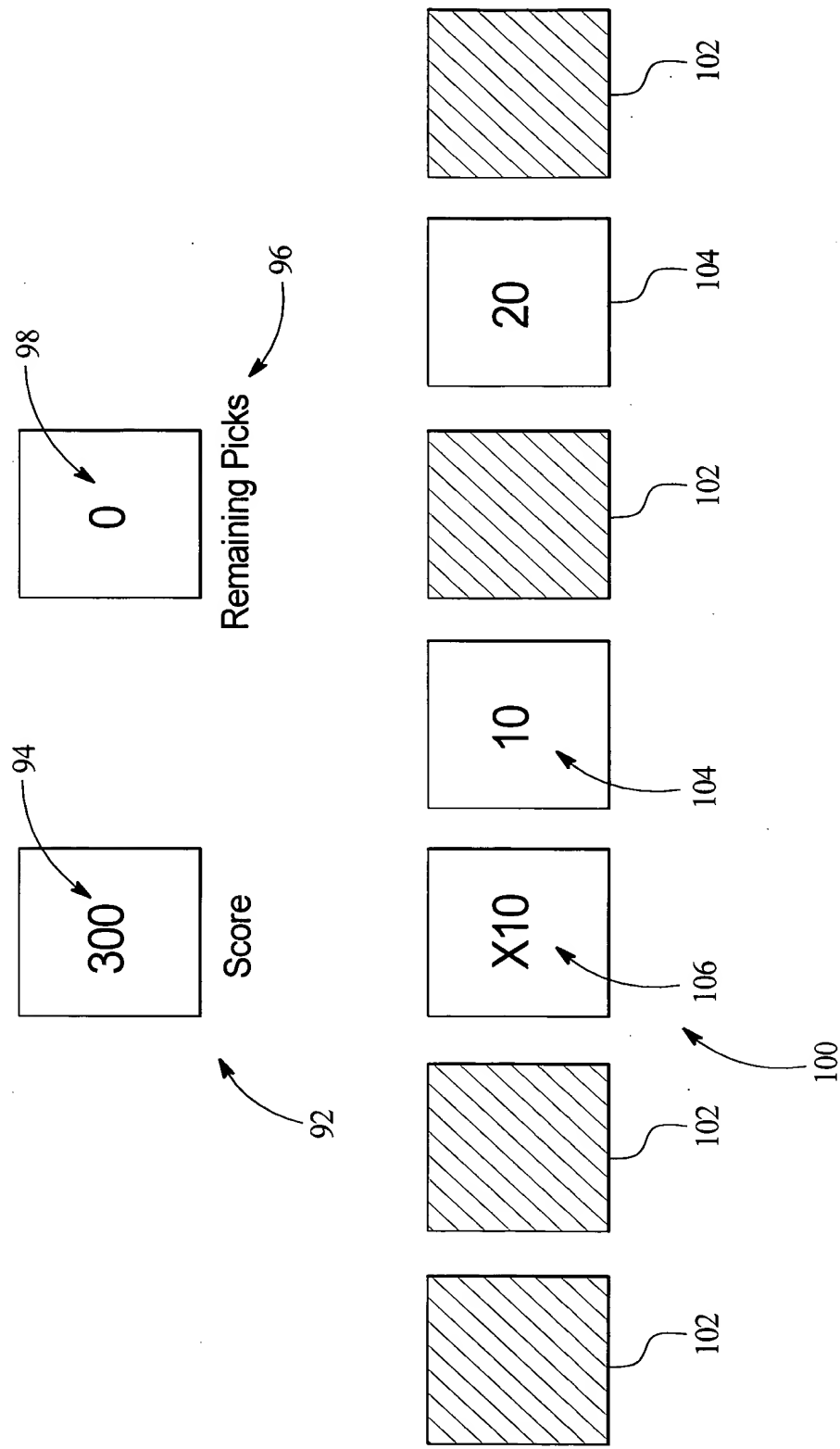
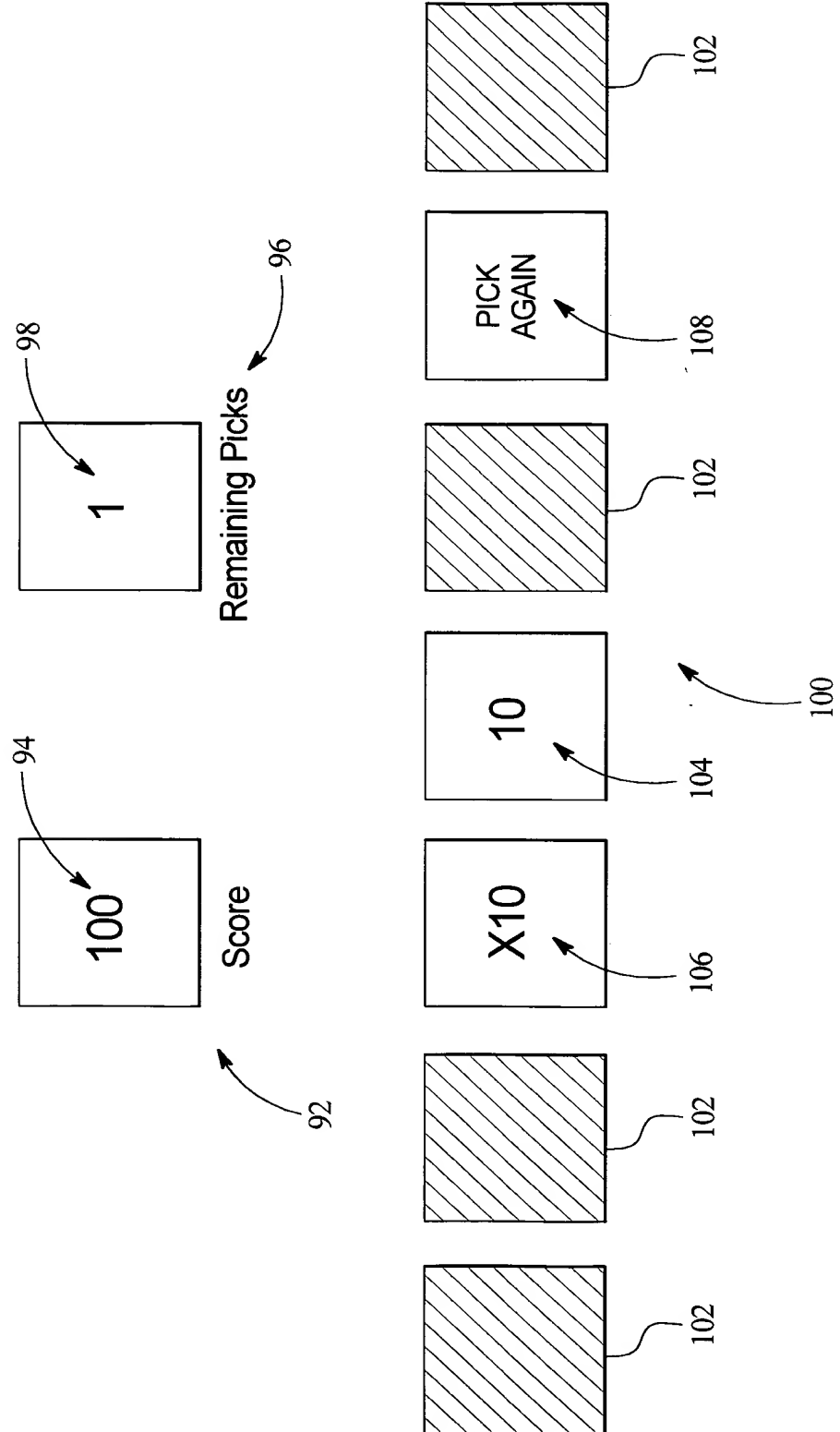




FIG. 6A





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FIG. 6B

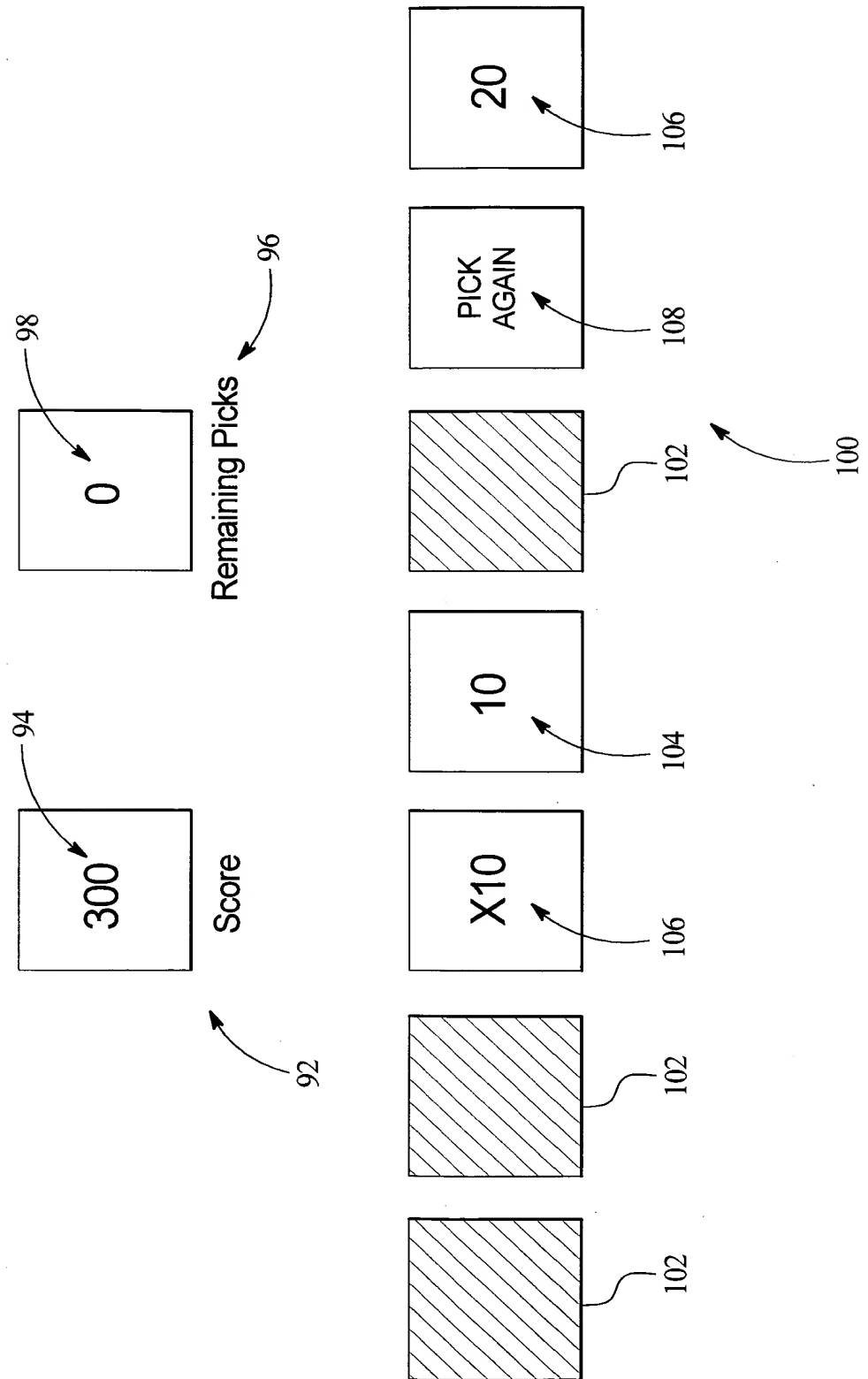




FIG. 7

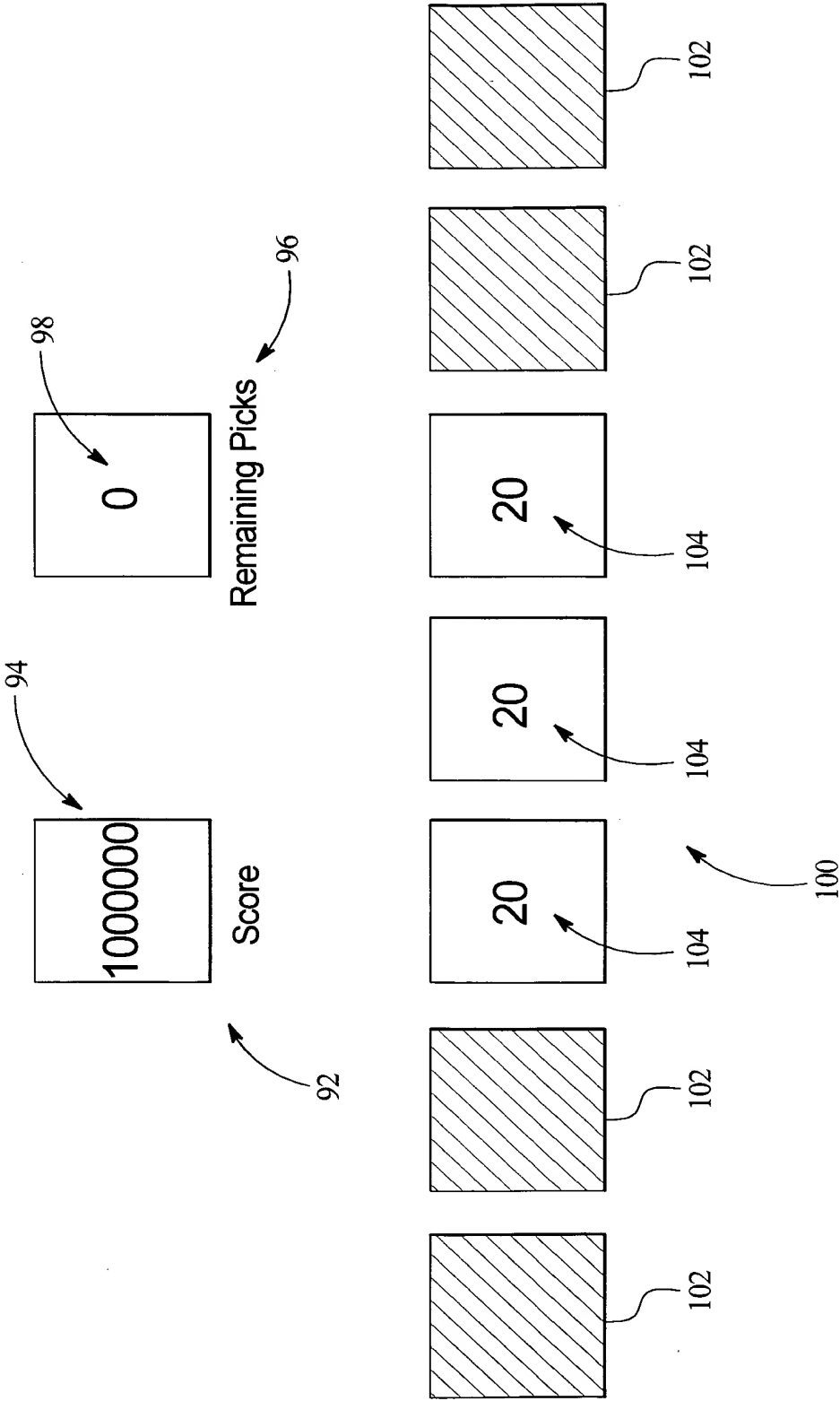




FIG. 8

